

CONCISE EXPLANATION UNDER RULE 98

“Advanced Technologies of Displays”, p.82-84, 101-106, Chizuka Tani, first print, first edition, Kyouritsu Publishing Company, Japan, December 28, 1998

A concise explanation of this document is given on pages 1-3 of the present specification.

JP-2003-521092A

This document shows a plasma display panel wherein a re-crystallizable glass is used as a binder for a paste for forming electrodes.

JP-2001-283742A

This document a plasma display panel wherein terminals of column electrodes, row electrodes and common electrodes are all formed on one plate.

JP-9-129143A

This document shows a plasma display device wherein glass frit sealing portions along the periphery of plates have a relatively small width, to reduce the dimension of seams between adjacent display surface areas in a tiled flat-panel display device, when the plasma display device is used as each unitary component of the tiled display device.

JP-2000-285816A

This document shows a tiled flat-panel display wherein glass plates are bonded together with glass frit, not only along the periphery of the plates, but also at internal partition walls, in order to reduce the width of the peripheral glass frit sealing portion.

JP-2003-91523A

This document shows an information server system including a plasma display panel (PDP) and a visual fluorescent display (VFD) connected to a network such as an internet

JP-2003-45345A

This document shows a display device such as VFD and FED wherein a front glass plate is formed with fluorescent light reflecting grooves which are utilized to accommodate a glass frit for bonding the front and back glass plates.

JP-2001-296836A

This document shows a plasma display device of AC type.

JP-2001-216905A

This document shows a plasma display panel wherein a glass frit is used to seal control electrodes as well as to bond together front and back glass plates so as to form an air-tight space.

JP-2001-135250A

This document shows a composition for forming a black matrix of a plasma display panel wherein the black matrix is embedded in a dielectric layer.